

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New Patent Application of)
Akira ISHIKAWA)
Japanese Priority Application No. 2002-284001) Attn: Applications
Japanese Priority Date: September 27, 2002) Branch
For: SEMICONDUCTOR DEVICE AND)
MANUFACTURING METHOD THEREOF) Date: September 24, 2003

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Reference 1, S. Kishino "Novel fundamental of semiconductor device" Ohmesha LTD, 1995, pp. 201-207 is described on page 2 of the specification, reciting the theory of hot carriers and LDD structure.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

By: _____


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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known			
				Application Number		New Application	
				Filing Date		September 24, 2003	
				First Named Inventor		Akira ISHIKAWA	
				Art Unit			
Examiner Name							
Sheet	1	of	1	Attorney Docket Number		740756-2653	

U.S. PATENT DOCUMENTS					
Examiner Initials ⁵	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US 2002/0008797 A1	01/24/2002	S. Yamazaki	
		US 2002/0110941 A1	08/15/2002	S. Yamazaki	
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		US 2002/0024051 A1	02/28/2002	S. Yamazaki	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials ⁵	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³	Number ⁴ Kind Code ² (if known)			

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ⁵	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		S. Kishino, "Novel fundamental of semiconductor device" Ohmesha LTD, 1995, pp. 201-207.	
		S. Ogura et al., "Design and Characteristic of the Lightly Doped Drain-Source (LDD) Insulated Gate Field-Effect Transistor". IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. ED-27, No. 8, 1980.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.